

# Bio-Data

**Name:** Prof (Dr) Rameshwar Rao  
**Nationality:** Indian

**Date of Joining:** **First Term :** 2001-2003  
**Head of the Dept:** **Second Term:** 2005-2006

**Present Position:** (a)Dean, Faculty of Engineering, OU, (b)Convener of PGECET 2011 & (c) Professor, Dept. of Electronics & Communication Engineering, Osmania University, Hyderabad - 500 007.

## **Educational Qualification**

1. B.E. (Electronics & Communication Engineering) Osmania University, Hyderabad.
2. M.Tech. (Communication) I.I.T., Bombay. (1982)
3. Ph.D., I.I.T., Bombay. (1990)

## **Awards & Honors:**

1. Awarded Sir Akbar Ali Hydari **Gold Medal** for securing first Rank in Faculty of Engineering, Osmania University for the year 1976.
2. Awarded Osmania University Students' **Gold Medal** for securing First Rank in the Dept. of Electronics & Communication, Osmania University, for the year 1976.
3. Awarded Sharad Kumar Pathak **Gold Medal** for securing highest marks in the Dept. of Electronics & Communication, and Dept. of Electrical Engg. Osmania University, for the year 1976.
4. Secured **Second State Rank** in Higher Secondary Multipurpose Examination, Government of Andhra Pradesh, in the year 1970.
5. Secured **First Rank** in Special Examination in Mathematics conducted by the University College of Engineering, Osmania University, 1971.

## **Experience:**

- **Research & Development:** Worked as Research & Development Engineer, for a period of 14 years and 9 months at the Communication Division, Avionics Design Bureau, Hindustan Aeronautics Limited, Hyderabad. A.P.- 500 042. Design and Development of Algorithms for Wireless Communication systems, Spread Spectrum systems and microprocessor based systems.
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- **Teaching** I have been offering the following courses at the Department of Electronics & Communication during the last 10 years. 1. Digital Electronics (UG) 2. Digital Communication (UG) 3. Modulation Theory & Techniques (UG) 4. Communication Systems (UG) 5. VHDL (UG) 6. Data Communication Networks (UG) 7. Project Coordinator (UG) 6. Algebraic Coding Theory (PG) 7. Digital Communications (PG) 8. Data & Computer Communication Networks (PG) 9. VHDL (PG). (10) VLSI Design (UG). (11) VLSI Design (PG).

## **Research Interests:**

VHDL Modeling & Synthesis, (During last three years) Data and Computer Communications, Detection and Estimation Theory, Information and Coding Theory, Microprocessor based applications, VLSI Design.

## **Laboratory Development:**

Responsible for modernization of Communication laboratory, setting up of VHDL laboratory. Awarded grants by AICTE, Government of India for Modernization of Communication Laboratory and setting up of VHDL and Embedded Systems Laboratory at the Department of Electronics & Communication Engineering, Osmania University, Hyderabad.

## **Short Term Courses: Coordinator:**

- (i) Short Term Course on Digital Communication at Hindustan Aeronautics Limited, Dec. 1991.

- (ii) Short Term Course on Digital Communication at Osmania University, July, 1995.
- (iii) Short Term Course on Digital Communication at Osmania University, July, 1996.
- (iv) Short Term Course on VHDL Modeling & Synthesis at ECIL, Hyderabad, 1997.
- (v) Short Term Course on VHDL Modeling & Synthesis at Osmania University.1999.
- (vi) Short Term Course for Motorola Engineers, Motorola India Ltd., Hyderabad, 1999.
- (vii) Short Term Course on VLSI Design Dec.2001.
- (viii) Short Term Refresher Course for HAL Engineers, June 2002.
- (ix) Short Term Course on Embedded Systems Nov. 2002
- (x) Short Term on Introduction to Embedded System Design & RTOS, July, 2003.
- (xi) Short Term Course on Embedded System Design, Dec. 2003.

**Visiting Fellowship:**

- Awarded Visiting Fellowship by M/s. **HBL SAB NIFE Power Systems**, R & D Division for development of embedded systems and DSP based signal processing hardware for Department of Telecommunication and Department of Railways, for a period of one year (16 Aug.1999 to 15th August 2000).
- Served as Visiting Research Professor/Adjunct Professor, Department of Electrical and Computer Engineering, **New Jersey Institute of Technology, Newark, NJ 07102 USA** and offered courses ECE394 and ECE689.

**Industry Academic Interaction:**

- Served as Co-ordinator for Project SSS (Self-Sustaining Scheme) funded by the Ministry of Information Technology, Government of India, for the period from Nov. 2001 to Dec. 2003.
- Served as Co-ordinator for Project Network funded by the Swiss Development and Cooperation agency from Nov.2001 to Dec. 2003.
- Co-ordinated workshop on Industry Institute Interface, Dec. 2002.
- Co-ordinated MS by Research Program in collaboration with ECIL

**Laboratory Development work under project TEQIP:**

Responsible for the development of

- (1) VLSI Design Laboratory
- (2) Embedded System Laboratory

at the Department of Electronics & Communication Engineering, Osmania University.

Laboratory curriculum developed using following tools:

- Active VHDL
- Keil Software for Embedded Systems (Intel based microcontrollers)
- Xilinx Foundation Software for FPGAs and CPLD Design
- Microwind for Backend VLSI design tools
- Metrowerks Code Warrior for Motorola based Embedded System Design
- Code Composer Studio for Texas Instruments DSP processors
- VxWorks for Real Time Operating Systems
- Training kits for programming FPGAs and CPLDs

**Delivered invited talks in reputed R&D institutions and has more than 60 conference/ journal publications.**

**4 research scholars obtained Ph.D. Degrees. At present 8 research scholars are pursuing Ph.D. program under his guidance.**